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**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

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**Complete if Known**

Application Number	09/629,337
Filing Date	August 1, 2000
First Named Inventor	Mark C. Fowler
Art Unit	2672
Examiner Name	Jeffery A. Brier
Attorney Docket Number	0100.0001160

Sheet 1 of 1

**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T <sup>2</sup>
		PINEDA, JUAN; A Parallel Algorithm for Polygon Rasterization; Computer Graphics; Volume 22, Number 4; August 1988; pages 17-20.	
		AKELEY, KURT; RealityEngine Graphics; Silicon Graphics Computer Systems; Computer Graphics Proceedings; Annual Conference Series; 1993; pages 109-116.	
		VAN HOOK, TIMOTHY J. et al.; Method and Apparatus for Graphics Rasterization; patent application filed December 28, 1999.	

Examiner Signature	Date Considered
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> Applicant is to place a check mark here if English language Translation is attached. This collection of information is required by 37 CFR 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.